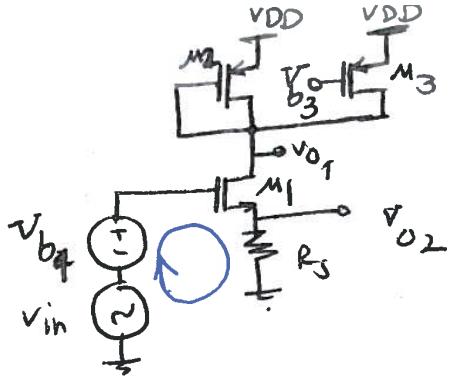


1)



Assignment 2

$$\begin{aligned}
 & I = 8 \text{ mA}, V_{DD} = 3 \text{ V}, V_{b3} = 1.9 \text{ V} \\
 & V_{t_n} = -0.5 \text{ V} \Rightarrow V_{t_p} = -0.6 \text{ V}, k'_n = \mu_n C_{ox} = 200 \text{ mA/V}^2 \\
 & \left(\frac{w}{l}\right)_1 = 40, k'_p = \mu_p C_{ox} = 100 \text{ mA/V}^2, \left(\frac{w}{l}\right)_2 = 40 \\
 & \left(\frac{w}{l}\right)_3 = 40, R_s = 50 \Omega
 \end{aligned}$$

a) if $I_1 = 1 \text{ mA} \Rightarrow V_{b1} = ?$

All Trans. are in Saturation

Solution: KVL around loop 1.

$$\begin{cases}
 V_{b1} = V_{gs1} + R_s I_1 \Rightarrow V_{b1} = 1 + \frac{50}{1000} \times 1 \text{ mA} = 1.05 \text{ V} \\
 I_1 = \frac{1}{2} k'_n \left(\frac{w}{l}\right)_1 (V_{gs1} - V_{t_n})^2 \Rightarrow \eta = \frac{1}{2} \times 2 \times 40 \times (V_{gs1} - 0.5)^2 \Rightarrow V_{gs1} = 1 \text{ V} \\
 V_{b1} = 1.05 \text{ V}
 \end{cases}$$

b) $A_{V1} = \frac{V_{o1}}{V_{in}} = ?$ The Impedance seen from the drain of Transistor M_2

$I_S = \frac{1}{g_{m2}}$ and That of M_3 is infinity

So, the parallel combination of M_2 and M_3 is $\frac{1}{g_{m2}}$

Using small signal model of M_1

$$\begin{aligned}
 & \text{Circuit diagram: Input } v_{in} \text{ through } g_{m1} V_{gs1} \text{ to } R_s \text{ and then to } g_{m2} V_{gs2} \text{ and finally to } V_{o1} \text{ through } \frac{1}{g_{m2}} \\
 & A_{V1} = \frac{V_{o1}}{V_{in}} = \frac{\frac{1}{g_{m2}} \times g_{m1}}{1 + g_{m1} R_s} =
 \end{aligned}$$

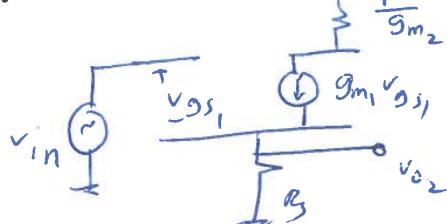
$$g_{m1} = \sqrt{2 \times 2 \times 40 \times 1} = 4 \text{ mA/V}$$

$$g_{m2} = \sqrt{2 \times 1 \times 40 \times \frac{1}{l_2}} = 2 \text{ mA/V}$$

$$I_{d2} = 1 - \frac{1}{2} \times 1 \times 40 \times (V_{DD} - 1.9 - 0.6)^2 = 0.5 \text{ mA}$$

$$\Rightarrow A_{V1} = \frac{4 \times \frac{1}{2}}{1 + (4 \times \frac{50}{1000})} = 1.67 \left(\frac{V}{V}\right)$$

c) $A_{V2} = \frac{V_{O2}}{V_{in}}$?? Using small signal model below.



$$A_{V2} = \frac{V_{O2}}{V_{in}} = \frac{g_{m1} R_S}{1 + g_{m1} R_S}$$

$$A_{V2} = \frac{4 \times \frac{50}{1000}}{1 + (4 \times \frac{50}{1000})} \approx 1.17 \quad (\text{V/V})$$

d) Output impedance seen at the output node V_{O1} .

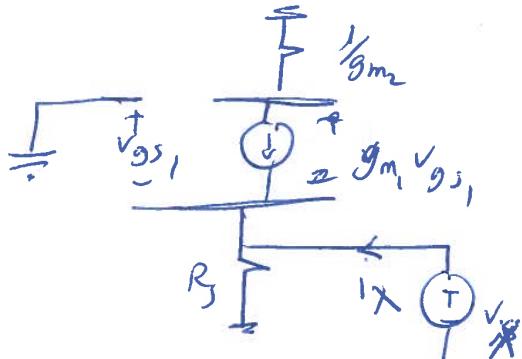
Because $\lambda = 0$ and therefore r_o of all transistors is moving towards ∞ ,

According to point (a), the impedance seen at node V_{O1} is

$$\frac{1}{g_{m2}} = 5 \text{ k}\Omega$$

e) Output impedance seen at node V_{O2} :

Using model below:



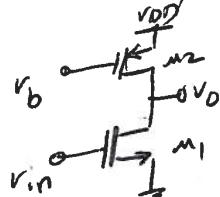
$$R_{O1} = \frac{V_X}{I_X} = R_S \parallel \frac{1}{g_{m1}}$$

$$R_{O1} = 50 \Omega \parallel 250 \Omega = 41.67 \Omega$$

2) Designing a Common-Source amplifier:

$$V_{DD} = 3V, P_{dc_total} = 1.5 \text{ mW}, V_{out, P-P} = 2.5V, A_v = 40, L = 4 \text{ mm}, V_{out, dc} = 1.5V$$

$$\lambda = \frac{1}{P_n} \cdot \frac{v^{-1}}{v_{tp}}, \quad \gamma_{50} = \frac{v_{tp}}{v_{t_n}} = \frac{v_{tp}}{v_{tp} - v_{t_n}} = \frac{1}{5}, \quad k_n' = 2 \frac{mA}{v^2}, \quad k_p' = 1 \frac{mA}{v^2}$$



Find V_B , dc level of input, w_1 , w_2 and $A_v = \frac{V_o}{V_{in}}$

Solution :

$$\left\{ \begin{array}{l} V_{out,pp} = V_{DD} - \frac{V_{ov_1}}{2} - V_{ov_2} = 2.5 \\ V_{dc_{out}} = \frac{V_{ov_1} + V_{DD} - V_{ov_2}}{2} = 1.5 \end{array} \right. \Rightarrow \frac{V_{ov_1}}{V_{ov_2}} = \frac{V_{ov_1}}{1.5} = 2.5$$

$$P_{DC} = 1.5 \times V_{DD} \times I \Rightarrow I = \frac{1.5}{3} = 0.5 \text{ mA}$$

$$r_{01} = r_{02} = \frac{1}{\lambda I} = \frac{1}{1 \times 5} = 20 \text{ kN}$$

$$|A_r| = g_{m_1} \times (r_0 || r_0) = g_{m_1} \times (20 || 20) \Rightarrow 40 = 10 \times g_{m_1} \Rightarrow g_{m_1} = 4 \text{ m/s}^2$$

$$g_{m_1} = \sqrt{2 \times 2 \times \left(\frac{\omega}{\zeta}\right)_1 \times 5^{mA}} = 4 \Rightarrow \left(\frac{\omega}{\zeta}\right)_1 = 80 \Rightarrow \omega_1 = 32 \text{ rad/s}$$

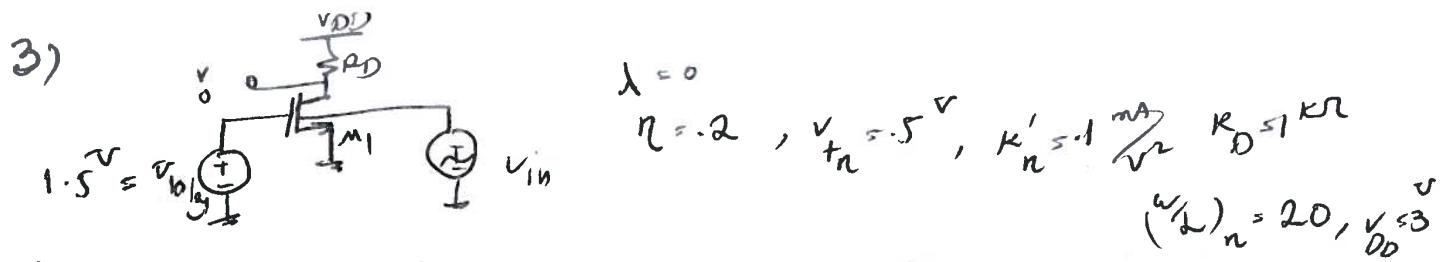
$$DC \text{ level of input: } V_{DC_1} = ? \Rightarrow 5^mA = \frac{1}{2} k_n' \times \left(\frac{V_{GS_1} - V_{TN}}{2}\right)^2$$

$$\Rightarrow \nu_{g_1} = \nu_{d_{c_1}} = .75^{\nu}$$

$$V_{O_{V_2}} = 2.5^V \Rightarrow I_D = \frac{1}{2} k' p \left(\frac{e}{2} \right) \frac{1}{2} (V_2 - 2.5^V)$$

$$V_{bias} = V_{DD} - V_{SG_2} = V_{DD} - V_{OR_2} - |V_{tp}| = 2.25 \text{ V}$$

This is an inverting amplifier whose gain by design has a magnitude of 40V/V so the gain will be -40V/V.

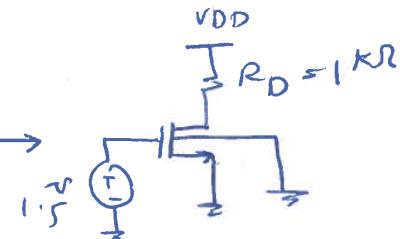


a) operating region of M_1 ?

DC model of structure above

$$V_{SB} = 0 \Rightarrow V_{TN} - V_{TN_0} = -0.5 \text{ V}$$

$$V_{DS} = 1.5 - 0.5 = 1 \text{ V}$$

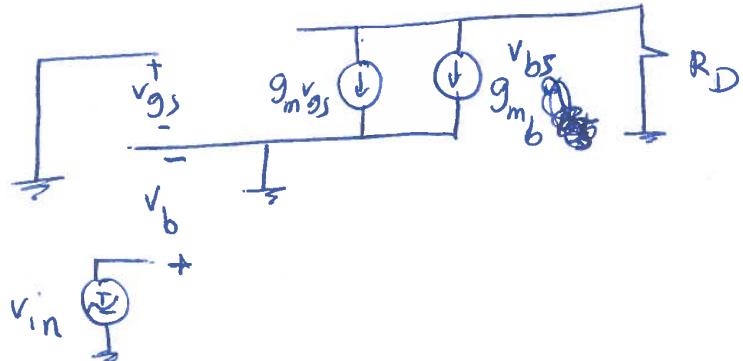


$$I_D = \frac{1}{2} \times 1 \times 20 \times (1)^2 = 1 \text{ mA}, V_{DS} = 3 - (1 \times 1) \Rightarrow V_{DS} = 2 \text{ V}$$

Since $V_{DS} < V_{DS}$ $\Rightarrow M_1$ is in saturation

$$b) A_v = \frac{V_{out}}{V_{in}} \quad g_{mb} = \eta g_m$$

Using small signal model

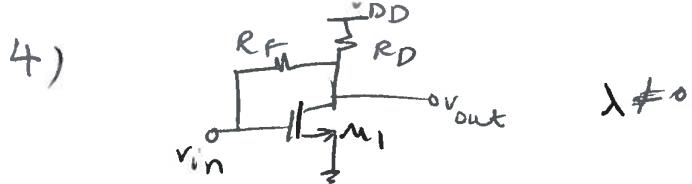


$$\rightarrow A_v = -g_{mb} R_D$$

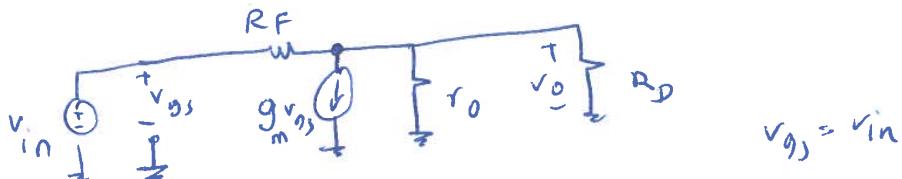
$$A_v = -\eta g_m R_D$$

$$g_m = \sqrt{2 \times 1 \times 20 \times 1} = 2 \text{ mA}$$

$$\rightarrow A_v = -0.2 \times 2 \times 1 = -0.4 \left(\frac{V}{V} \right)$$



a) $A_V = ?$ Using small signal model:



having a kcl at node V_{out} : $\frac{v_o - v_{in}}{R_F} + \frac{v_o}{r_o} + \frac{v_o}{R_D} + g_m v_{in} = 0$

$$\Rightarrow v_o \left\{ \frac{1}{R_F} + \frac{1}{r_o} + \frac{1}{R_D} \right\} = v_{in} \left\{ \frac{1}{R_F} - g_m \right\} \Rightarrow$$

$$A_V = \frac{\frac{1}{R_F} - g_m}{\frac{1}{R_F} + \frac{1}{r_o} + \frac{1}{R_D}}$$

b) $R_F = ?$ if $A_V = 1$: using A_V calculated in the last part

$$\frac{1}{R_F} - g_m = \frac{1}{R_F} + \frac{1}{r_o} + \frac{1}{R_D}$$

The only value of RF for which the gain will be one is $RF=0$ which is a trivial solution that is input and output are shorted. Otherwise, there is no non-zero solution for RF.

c) $R_F = ?$ if $A_V = -1 \Rightarrow g_m - \frac{1}{R_F} = \frac{1}{R_F} + \frac{1}{r_o} + \frac{1}{R_D} \Rightarrow g_m - \frac{1}{r_o} - \frac{1}{R_D} = \frac{2}{R_F}$

$$R_F = \frac{2}{g_m - \frac{1}{r_o} - \frac{1}{R_D}}$$

d) for R_F to be ~~realizable~~ ^{realizable}: $R_F > 0 \Rightarrow \frac{2}{g_m - \frac{1}{r_o} - \frac{1}{R_D}} > 0 \Rightarrow g_m > \frac{1}{r_o} + \frac{1}{R_D}$