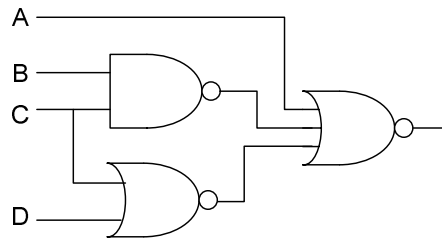10) Consider the following circuit.

a) Assuming the vectors (A=0,B=0,C=0,D=0) and (A=0,B=1,C=1,D=1) are applied (in that order). During testing, you only have access to the single output signal. What is the fault coverage obtained (assuming a single stuck-at fault model)?   Show your work. [4 marks]